IN THE SPECIFICATION:

Please replace the Summary of Invention section on page 13, lines 1-27:

--An object of the present invention is to provide an image pickup device which can achieve the reduction of the sizes of pixels.

For achieving the object mentioned above, as an embodiment, the present invention provides an image pickup device including an array of a plurality of pixels including photoelectric conversion portions for accumulating signal charges generated by photoelectric conversion and an amplifying transistor for amplifying the signal charges generated by the photoelectric conversion portion and outputting the amplified signal charges, comprising:

a junction-type field effect transistor, including a main electrode region made of a first semiconductor region of a first conduction type connected to control electrode region of the amplifying transistor included in two pixels adjacent to each other, and a control electrode region made of a second semiconductor region of a second conduction type opposite to the first conductivity type having same electric potential as that of semiconductor region of the second conduction type included in a semiconductor region forming the photoelectric conversion portions, the junction type field effect transistors arranged in a same line connecting to each other in series; ~~and~~

an electric potential supplying circuit for supplying predetermined electric potential to the main electrode region of the junction-type field effect transistor; and

a potential control circuit for controlling electric potential of the first semiconductor region by means of capacity coupling.--